

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	404529	capacitor	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L2	483974	transistor	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L3	272226	inverter inversion	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L4	3693	(345/98-100). CCLS.	US-PGPUB; USPAT	OR	OFF	2008/04/17 09:38
L5	10926	sampling adj circuit	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L6	262	L4 and L5	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L7	161	L6 and L1 and L2 and L3	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L8	286	feed adj through adj voltage	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L9	13782	(345/87- 102,204). CCLS.	US-PGPUB; USPAT	OR	OFF	2008/04/17 09:38
L10	404529	capacitor	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L11	483974	transistor	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L12	272226	inverter inversion	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L13	2581	L9 and L10 and L11 and L12	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L14	72492	L12 with (L10 L11)	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L15	900	L13 and L14	US-PGPUB; USPAT	OR	ON	2008/04/17 09:38
L16	17182	sampling adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 09:38

L17	107	L15 and L16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 09:38
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4/17/2008 9:54:23 AM

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